ECE 3730 – Principles of Embedded Systems Design  Winter 2019

Course Objectives

This course will introduce students to the design and implementation of embedded systems. Topics include introduction to UML and data structures, A2D, D2A, serial bus architectures (SPI and I²C), embedded computing, bus-based computer systems, program design and analysis, networks, and hardware-software co-design.

Course Content

The following topics will be covered:

• Embedded systems design process and principles
• Formalisms for system design
• Processors and/or FPGA based system implementation
• Inter-component communications
• Program design and analysis
• Data structures.

Textbook


Other Resources


Academic Integrity

Students are expected to conduct themselves in accordance with the highest ethical standards of the Profession of Engineering and evince academic integrity in all their pursuits and activities at the university. As such, in accordance with the General Academic Regulations on Academic Integrity, students are reminded that plagiarism or any other form of cheating in examinations, term tests, assignments, projects, or laboratory reports is subject to serious academic penalty (e.g. suspension or expulsion from the faculty or university). A student found guilty of contributing to cheating by another student is also subject to serious academic penalty.

Retention of Student Work

Students are advised that copies of their work submitted in completing course requirements (i.e. assignments, laboratory reports, project reports, test papers, examination papers, etc.) may be retained by the instructor and/or the department for the purpose of student assessment and grading, and to support the ongoing accreditation of each Engineering program. This material shall be handled in accordance with the University’s Intellectual Property Policy and the protection of privacy provisions of The Freedom of Information and Protection of Privacy Act (Manitoba). Students who do not wish to have their work retained must inform the Head of Department, in writing, at their earliest opportunity.
Learning Outcomes

1. Demonstrate the ability to model the design of an embedded system using UML diagrams.
2. Demonstrate the ability to use the C language to write programs for embedded systems.
3. Demonstrate the ability to apply C linked-list, pointer, and data structures in embedded applications.
4. Demonstrate the ability to apply COTS components (A2D, D2A, configurable real-time clock, rotary encoder, LCD, GPIO expander, and FIFO memory) in the design of various embedded systems applications (device tuner, arbitrary waveform generator, signal filter, error detection and correction, and LCD display system).
5. Apply the SPI and I2C inter-component communications protocols in embedded systems.

Expected Competency Levels

<table>
<thead>
<tr>
<th>Outcome</th>
<th>A1</th>
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Evaluation

Students must satisfy each evaluation component in the course to receive a final grade. This means that students must complete and pass all laboratories, projects, and examinations to be eligible to receive a passing grade. The final course grade is determined by the student’s performance in the following:

<table>
<thead>
<tr>
<th>Component</th>
<th>Value (%)</th>
<th>Method of Feedback</th>
<th>Learning Outcomes Evaluated</th>
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<tbody>
<tr>
<td>Projects: Laboratories and Assignments</td>
<td>30</td>
<td>F, S</td>
<td>1, 2, 3, 4, 5</td>
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<td>Term Test</td>
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<td>F, S</td>
<td>2, 3, 4, 5</td>
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<td>Final Examination</td>
<td>50</td>
<td>S</td>
<td>1, 2, 3, 4, 5</td>
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* Method of Feedback: F - Formative (written comments and/or oral discussion), S - summative (numerical grade)

Requirements/Regulations

- Attendance at lectures and laboratories is essential for successful completion of this course. Students must satisfy each evaluation component in the course to receive a final grade.
- It is the responsibility of each student to contact the instructor in a timely manner if he or she is uncertain about his or her standing in the course and about his or her potential for receiving a failing grade. Students should also familiarize themselves with the University’s General Academic Regulations, as well as Section 3 of the Faculty of Engineering Academic Regulations dealing with incomplete term work, deferred examinations, attendance and withdrawal.
- No programmable devices or systems (such as calculators, PDAs, iPods, iPads, cell phones, smart watches, wireless communication or data storage devices) are allowed in examinations unless approved by the course instructor.
- Students should be aware that they have access to an extensive range of resources and support organizations. These include Academic Resources, Counselling, Advocacy and Accessibility Offices as well as documentation of key University policies e.g. Academic Integrity, Respectful Behaviour, Examinations and related matters.

Supplemental Information