



Course Outline

Instructor

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Office Hours

- By appointment

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Contact Hours

- 4 credit hours
- Lectures:
3 hours x 13 weeks = 39 hours
- Laboratories:
3 hours x 5 weeks = 15 hours

Prerequisites:

- ECE 2160 Electronics 2E

Course Website:

<https://umanitoba.ca/umlearn>

Important Dates

- **Term Test**
October 23rd, 2020
6:00pm – 8:00pm
- **Voluntary Withdrawal Deadline**
November 23rd, 2020
- **Thanksgiving Day**
October 12th, 2020
No classes or examinations
- **Remembrance Day**
November 11th, 2020
No classes or examinations
- **Fall Term Break**
November 9th–13th, 2020
No classes or examinations

ECE 3670 – Electronics 3E

Fall 2020

IMPORTANT NOTICE – Mandatory Requirement to Report

This course will be conducted using remote instruction. Students who are accessing the course from outside of Canada or the USA **must notify the instructor** and indicate in which country they are located. Access to software may be restricted from some countries and failure to comply with these restrictions may result in criminal prosecution.

Course Objectives

As a continuation of ECE 2160 Electronics 2E, the objectives of this course are for students to learn to both analyze and design fundamental electronic circuits, and to explore their practical applications.

Course Content

The following topics will be covered:

- MOSFETs
- Differential Amplifiers: Common-mode vs. differential mode, common-mode rejection ratio, small-signal and large-signal operation, input/output characteristics, non-ideal characteristics, active load, frequency response.
- Single-stage IC Amplifiers: BJT, biasing, high-frequency response, large-signal transfer characteristics, follower circuits, paired transistors, current mirror.
- Feedback: Theory, feedback topologies, examples of feedback circuits, circuit characteristics improvement using feedback.
- Oscillators: Loop-gain criteria, Wien-Bridge oscillators, tank circuit/tuned circuit oscillators, crystal oscillators, multi-vibrators, timers.
- Power Amplifiers: Classification, some common configurations, detailed class B power amplifiers.
- Digital Circuits: Inverter characteristic, noise margins, loading and fan-out, power dissipation.

Textbook

Microelectronic Circuits, A.S. Sedra and K.C. Smith, Oxford University Press, 7th edition.

Other References

The Arts of Electronics, P. Horowitz and W. Hill, Cambridge University Press, 2nd edition, 1989.

Learning Outcomes

1. Analyze and design differential amplifiers.
2. Analyze and design single stage amplifiers.
3. Analyze and design feedback and oscillators.
4. Analyze and design power amplifiers.
5. Analyze digital circuits.

Expected Competency Levels

Outcome	KB	PA	IN	DE	ET	IT	CS	PR	IE	EE	EP	LL
1	3	4	2	4	3	3	2	2		1	2	3
2	3	4	2	4	3	3	2	2		1	2	3
3	3	4	2	4	3	3	2	2		1	2	
4	3	4	2	4	3	3	2	2		1	2	
5	3	4	2	4	3	3	2	2		1	2	

Accreditation Details

Accreditation Units

- Mathematics: 0%
- Natural Science: 0%
- Complementary Studies: 0%
- Engineering Science: 67%
- Engineering Design: 33%

Graduate Attributes

KB: A knowledge base for engineering
PA: Problem analysis
IN: Investigation
DE: Design
ET: Use of engineering tools
IT: Individual and team work
CS: Communication skills
PR: Professionalism
IE: Impact of engineering on society/
environment
EE: Ethics and equity
EP: Economics and project
management
LL: Life-long learning

Competency Levels

- 1 - Knowledge (Able to recall information)
- 2 - Comprehension (Ability to rephrase information)
- 3 - Application (Able to apply knowledge in a new situation)
- 4 - Analysis (Able to break problem into its components and establish relationships.)
- 5 - Synthesis (Able to combine separate elements into a whole)
- 6 - Evaluation (Able to judge the worth of something)

Grading Scale

Letter	Mark
A+	95–100
A	85–94
B+	80–84
B	70–79
C+	65–69
C	55–64
D	45–54
F	< 45

Note: These boundaries represent a guide for the instructor and class alike. Provided that no individual student is disadvantaged, the instructor may vary any of these boundaries to ensure consistency of grading from year-to-year.

Evaluation

The final course grade will be determined from a student's performance on assignments, in laboratories (including a design project), and on examinations. Students must receive a minimum of 50% on the final examination in order to be eligible to receive a passing grade. Students who are unable to write the mid-term exam for medical (or other acceptable) reasons will be assigned a grade based on their performance on the other assessment components (scaled to 100%). Calculators (incapable of communicating with other devices) are allowed in the mid-term and final exams. Students must complete all the laboratories in order to be eligible to receive a passing grade.

Component	Value (%)	Method of Feedback	Learning Outcomes Evaluated
Quizzes (best 4 of 5)	10	S	1, 2, 3, 4, 5
Laboratories + Project	20	F, S	1, 2, 3, 4, 5
Term Test ⁽¹⁾	30	F, S	1, 2, 3, 4, 5
Final Examination ⁽¹⁾	40	S	1, 2, 3, 4, 5

* The midterm and final will include approximately 50% design problems (DE).

* Method of Feedback: F - Formative (written comments and/or oral discussion), S - summative (numerical grade)

CEAB Graduate Attributes Assessed

PA.3 – Analyzes and solves complex engineering problems.

DE.2 – Uses an appropriate design process that considers all relevant factors (i.e., health & safety risks; standards; economic, environmental, cultural and societal considerations).

Academic Integrity

Students are expected to conduct themselves in accordance with the highest ethical standards of the Profession of Engineering and evince academic integrity in all their pursuits and activities at the university. As such, in accordance with the *General Academic Regulations on Academic Integrity*, students are reminded that plagiarism or any other form of cheating in examinations, term tests, assignments, projects, or laboratory reports is subject to serious academic penalty (e.g. suspension or expulsion from the faculty or university). A student found guilty of contributing to cheating by another student is also subject to serious academic penalty.

Requirements and Regulations

- Attendance at lectures and laboratories is essential for successful completion of this course. Students must satisfy each evaluation component in the course to receive a final grade.
- It is the responsibility of each student to contact the instructor in a timely manner if he or she is uncertain about his or her standing in the course and about his or her potential for receiving a failing grade. Students should also familiarize themselves with the University's *General Academic Regulations*, as well as Section 3 of the Faculty of Engineering *Academic Regulations* dealing with incomplete term work, deferred examinations, attendance and withdrawal.
- No programmable devices or systems (such as calculators, PDAs, iPods, iPads, cell phones, wireless communication or data storage devices) are allowed in examinations unless approved by the course instructor.
- Students should be aware that they have access to an extensive range of resources and support organizations. These include Academic Resources, Counselling, Advocacy and Accessibility Offices as well as documentation of key University policies e.g. Academic Integrity, Respectful Behaviour, Examinations and related matters.

 [Supplemental Information](#)

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Retention of Student Work

Students are advised that copies of their work submitted in completing course requirements (i.e. assignments, laboratory reports, project reports, test papers, examination papers, etc.) may be retained by the instructor and/or the department for the purpose of student assessment and grading, and to support the ongoing accreditation of each Engineering program. This material shall be handled in accordance with the University's *Intellectual Property Policy* and the protection of privacy provisions of *The Freedom of Information and Protection of Privacy Act (Manitoba)*. Students who do not wish to have their work retained must inform the Head of Department, in writing, at their earliest opportunity.